

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	STRUCTURE AND METHOD FOR SILICIDED METAL GATE TRANSISTORS						
<p>Application Number : 10/605,130</p> <p>Confirmation Number:</p> <p>First Named Applicant: Bruce Doris</p> <p>Attorney Docket Number: FIS920030283US1</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: ( 6033963 or 6492694 or 6528362 or 6534352 or 6544827 or 6589866 or 20030122202 ).pn</p>							
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
DN	1	6033963	2000-03-07	Huang, et al.			---
	2	6492694	2002-12-10	Nobel, et al.			---
	3	6528362	2003-03-04	Besser, et al.			---
	4	6534352	2003-03-18	Kim			---
	5	6544827	2003-04-08	Abiko			---
DN	6	6589866	2003-07-08	Besser, et al.			---
<b>US Published Applications</b>							
Note: Applicant is not required to submit a paper copy of cited US Published Applications							
init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
DN	1	20030122202	2003-07-03	Kwon			---
<b>Signature</b>							
Examiner Name				Date			
				10/6/04			